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IN THE CLAIMS:

All pending claims are set forth below. Cancelled and withdrawn claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with strikethrough. The status of each claim is indicated with one of (original), (currently amended), (previously amended), (cancelled), (withdrawn), (new), (previously added), (reinstated - formerly claim #), (previously reinstated), (re-presented - formerly dependent claim #), or (previously re-presented). Please AMEND claims 1,2, 6, 8, 13, 14, and 16-18, and ADD new claim 19 in accordance with the following:

Please CANCEL claims 3-5, 7 and 11 without prejudice and disclaimer.

1. (CURRENTLY AMENDED) A differential signal output apparatus comprising:
a first differential pair for receiving differential signals, said first differential pair having, first transistors;
a first current source, connected to one end of the first differential pair at a first branching node branching current from the first current source to the first transistors; for supplying a current to the differential pair, and
a second differential pair receiving the differential signals, said second differential pair having second transistors;
a second current source connected to one end of the second differential pair at a second branching node branching current from the second current source to the second transistors; and
a capacitor connected between the first branching node and the second branching node so that the capacitor, the first current source, and the second current source are connected in series for branching from the current source to transistors and a low impedance node.
2. (CURRENTLY AMENDED) The differential signal output apparatus, as claimed in Claim 1, wherein:
the capacitor, the first current source, and the second current source are connected between the a first low impedance node and a second low impedance node is a power-supply voltage or a ground voltage.

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3. (CANCELED)

4. (CANCELED)

5. (CANCELED)

6. (CURRENTLY AMENDED) The differential signal output apparatus, as claimed in Claim 1, wherein:

the capacitor forms a current path for letting allowing the current supplied from one of the first and second current sources to flow when the current to one of the first and second differential pair is cut off.

7. (CANCELED)

8. (CURRENTLY AMENDED) A differential signal output apparatus comprising:
a first differential pair receiving differential signals, said first differential pair having first transistors;

a first current source, connected to one end of the first differential pair at a first branching node branching current from the first current source to the first transistors;

a second differential pair receiving the differential signals, said second differential pair having second transistors;

a second current source connected to one end of the second differential pair at a second branching node branching current from the second current source to the second transistors;

a differential pair for receiving differential signals;

a current source, connected to one end of the differential pair, for supplying a current to the differential pair; and

a transitional response circuit for forming a current path for letting allowing one of the current supplied from one of the first and second current sources to flow when the current to one of the first and second differential pair is cut off.

9. (ORIGINAL) The differential signal output apparatus, as claimed in Claim 8, wherein:
the transitional response circuit is a capacitor.

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10. (ORIGINAL) The differential signal output apparatus, as claimed in Claim 6, wherein:

the impedance of the capacitor is smaller than the load impedance in the differential signal output apparatus at a transitional response frequency at which a transitional current flows to the capacitor.

11. (CANCELED)

12. (ORIGINAL) The differential signal output apparatus, as claimed in Claim 9, wherein:

the impedance of the capacitor is smaller than the load impedance in the differential signal output apparatus at a transitional response frequency at which a transitional current flows to the capacitor.

13. (CURRENTLY AMENDED) A semiconductor integrated circuit apparatus provided with a differential output circuit comprising:

a first differential pair constituted by arranging wiring between differential input signals and between differential output signals and arranging first transistors symmetrically;

a first current source connected to one end of the first differential pair and so arranged that first connection wiring lines to the first transistors be symmetrical; and

a second differential pair constituted by arranging wiring between the differential input signals and the differential output signals arranging second transistors symmetrically.

a second current source connected to one end of the second differential pair so arranged that second connection wiring lines to the second transistors be symmetrical; and

a capacitor connected between a first branching node and a second branching node for the first and second branching connection wiring lines from the first current source and the second current source to the first and second transistors and a low impedance node and arranged in an area between the first and second transistors.

14. (CURRENTLY AMENDED) A semiconductor integrated circuit apparatus provided with a differential output circuit comprising:

a first differential pair constituted by arranging wiring between differential input signals and between differential output signals and arranging first transistors symmetrically;

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a first current source connected to one end of the first differential pair and so arranged that first connection wiring lines to the first transistors be symmetrical;

a second differential pair constituted by arranging wiring between the differential input signals and the differential output signals arranging second transistors symmetrically;

a second current source connected to one end of the second differential pair so arranged that second connection wiring lines to the second transistors be symmetrically; and

a capacitor connected between a first branching node and a second branching node for the first and second branching connection wiring from the first current source and the second current source to the first and second transistors and a current supply unit having a current supply capacity equal to or greater than the amperage supplied by the first current source and arranged with the same symmetry as the symmetry of arrangement between the transistors arranged in an area between the first and second transistors,

wherein a current supply ability of the second current source is same as or greater than a current supply ability of the first current source.

15. (ORIGINAL) A semiconductor integrated circuit apparatus provided with a differential output circuit comprising:

a first differential pair constituted by arranging wiring between differential input signals and between differential output signals and arranging transistors of a first conductivity type symmetrically;

a first current source connected to one end of the first differential pair and so arranged that connection wiring lines to the transistors of the first conductivity type be symmetrical;

a second differential pair arranged opposite to the first differential pair, constituted by arranging wiring between differential input signals and between differential output signals and arranging transistors of a second conductivity type symmetrically;

a second current source connected to one end of the second differential pair and so arranged that connection wiring lines to the transistors of the second conductivity type be symmetrical; and

a capacitor connected between a first branching node for branching connection wiring from the first current source to the transistors of the first conductivity type and a second branching node for branching connection wiring from the second current source to the transistors of the second conductivity type and arranged in an area surrounded by the first differential pair and the second differential pair.

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16. (CURRENTLY AMENDED) A differential signal transmission system provided with a differential output circuit, the differential output circuit comprising:

a first differential input unit into which differential signals are entered;
a first current supply unit ~~for~~ supplying a current to the first differential input unit;
a second differential input unit into which the differential signals are entered;
a second current supply unit supplying a current to the second differential input unit; and
a capacitor connected between a first connection node between the first differential input unit and the first current supply unit and a ~~low-impedance node~~ a second connection node ~~between the second differential input unit and the second current supply unit so that the capacitor, the first current supply unit, and the second current supply unit are connected in series.~~

17. (CURRENTLY AMENDED) A differential signal transmission system provided with a differential output circuit, the differential output circuit comprising:

a first differential input unit into which differential signals are entered;
a first current supply unit ~~for~~ supplying a current to the first differential input unit;
a second differential input unit into which the differential signals are entered;
a second current supply unit supplying a current to the second differential input unit; and
a capacitor connected between a first connection node and a second connection node, ~~the first and second connection nodes respectively connecting the first current supply unit to the first differential input unit and the second current supply unit to the second differential input unit~~ ~~between the differential input unit and the first current supply unit and a second current supply unit having a current supply capacity equal to or greater than the amperage supplied by the first current supply unit~~

wherein a current supply ability of the second current supply unit is same as or greater than a current supply ability of the first current supply unit.

18. (CURRENTLY AMENDED) A differential signal transmission system provided with a differential

output circuit, the differential signal transmission system comprising:

a first differential input unit configured in a first conductivity type for entering differential signals;

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a first current supply unit for supplying a current to the first differential input unit;
a second differential input unit configured in a second conductivity type, of which differential output terminals are connected to the differential output terminals of the first differential input unit to receive the differential signals;
a second current supply input unit for supplying a current to the second differential input unit; and
a capacitor connected between a connection node between the first differential input unit and the first current supply input unit, and another connection node between the second differential input unit and the second current supply input unit.

19. (NEW) A differential signal output apparatus comprising:
a first differential pair to receive differential signals;
a first current source connected to the first differential pair at a first branching node;
a second differential pair to receive the differential signals;
a second current source connected to the second differential pair at a second branching node; and
a capacitor connected between the first branching node and the second branching node to connect the capacitor, the first current source, and the second current source in series.